## PECEIVED CENTRAL FAX CENTER

Serial No. 10/734,429

## JAN 24 2007

## REMARKS

In a Final Office Action dated September 25, 2006 (hereinafter the "Office Action") various formalities were raised with respect to claims 7, 8, 20 and 23. Applicant thanks Examiner for the corrections and has corrected the claims to address the issues.

In the Office Action, claims 1-3, 7, 8, 11-17, and 20-26 were rejected under 35 U.S.C. 103(a) as being unpatentable over Kawase 2003/0141807 in view of admitted prior art. Applicant's arguments regarding coupling were deemed unpersuasive in view of Figures 2 and 4 of Kawase. Applicant respectfully disagrees. Figure 2 of Kawase does not even show a transistor and Figure 4 of Kawase shows using two transistors coupled to each other, the drain of one coupled to the gate of a second transistor. The second transistor may have the channel width longer then the shorter of the two pixel dimensions. However the Kawase second transistor has a source electrode coupled to a supply voltage (not equivalent to a data line) and a gate line coupled to the drain of the first transistor (not equivalent to a gate line). Thus Kawase is insufficient to render a prima facie case of obviousness.

Applicant further respectfully submits that Kawase is insufficient to render obvious Applicant's invention. Kawase's design requires two transistors which is more difficult to fabricate then Applicant's one transistor design. Furthermore, Kawase's design necessitates additional driver electronics for the data signal.

Applicant also further respectfully submits that other limitations recited in the dependent claims are not rendered obvious by Kawase. For example, with respect to claim 2 and 26 Applicant respectfully submits that it is insufficient to recite that the chosen dimensions are obvious to achieve the objectives specified in Applicant's disclosure. A prima facie case of obviousness requires that the limitation be shown in the prior art. Claim 24 is not rendered obvious because as previously described, the gate line of the transistor in Kawase is coupled to drain of the first transistor. No mention is made of claim 13, but Applicant respectfully submits that claim 13 is also not

shown in Kawase. In order to further define the Applicant's structure, claim 1 has been further amended and claim 33 added.

Applicant called Examiner on January 23 requesting an interview to discuss the Office Action. Examiner indicated that because a final had been issued, an interview was not appropriate and instead suggested that Applicant amend the claims, file a request for continued examination and request an interview in the "remarks". Thus Applicant files herewith a request for continued examination and requests that the Examiner contact applicant at the number below at Examiner's earliest convenience to discuss the case.

In view of the preceding amendments and remarks, Applicant respectfully submits that all the case is in condition for allowance. Applicant respectfully requests allowance at the Examiner's earliest convenience.

No additional fee is believed to be required for this amendment. However, the undersigned Xerox Corporation Attorney hereby authorizes the charging of any necessary fees, other than the issue fee, to Xerox Corporation Deposit Account No. 24-0025. This also constitutes a request for any needed extension of time and authorization to charge all fees therefor to Xerox Corporation Deposit Account No. 24-0025.

In the event that the Examiner believes a teleconference would facilitate prosecution, Applicant respectfully requests that Examiner contact the undersigned.

Respectfully submitted,

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